

Amendments to the Abstract:

~~There is provided a semiconductor integrated circuit device which assures high performance and low power consumption through reduction of installation area and realizes automatic voltage adjustment of a couple of voltage step-down power supply circuits for active and standby conditions.~~ In this a semiconductor integrated circuit device, having a pair of voltage step-down power supply circuits for active and standby conditions, a first reference voltage is formed by amplifying a fixed voltage formed in a fixed voltage generating circuit with an amplifying circuit which can adjust the voltage gain ~~with~~ having a resistance circuit and a switch controlled with a first trimming switch setting signal. A_n , ~~an~~ an internal step-down voltage₁ when the internal circuit is in the active condition₁ is outputted from a first output buffer₁ which is activated with a first control signal. A_s , a second reference voltage is formed by adjusting a combination of ~~the~~ threshold voltages of MOSFETs ~~with a plurality of MOSFETs~~ and a switch controlled with a second trimming switch setting signal₁, and an internal step-down voltage₁ when the internal circuit is in the standby condition₁ is outputted with a second output buffer₁ which is activated with a second control signal.